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## KRISHNA R. NARAYANAN

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213 Cecilia Ct,  
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TX 77845, USA

**Education**     **Ph.D. in Electrical Engineering**, Dec 1998  
Georgia Institute of Technology, Atlanta, GA

**M.S. in Electrical Engineering**, Aug 1994  
Iowa State University, Ames, IA

**B.S. in Electrical Engineering**, May 1992  
Coimbatore Institute of Technology, India  
Ranked First out of 47 Students

**Work Experience**     *September 2008 to Present* - **Professor**, Department of Electrical and Computer Engineering, Texas A&M University, College Station, TX 77843

*July 2012 to Dec 2014* - **Director of Graduate Studies**, Department of Electrical and Computer Engineering, Texas A&M University, College Station, TX 77843

*September 2003 to August 2008* - **Associate Professor**, Department of Electrical and Computer Engineering, Texas A&M University, College Station, TX 77843

*December 1998 to August 2003* - **Assistant Professor**, Department of Electrical and Computer Engineering, Texas A&M University, College Station, TX 77843

*June '95 to September '95* - **Summer Intern**, AT&T Bell Laboratories, Crawford Hill Laboratory, NJ 07733

**Visiting Positions**     January 2015 to May 2015, Department of Electrical Engineering and Computer Science, University of California at Berkeley.

January 2005 to May 2005, Institut Eurecom, Sophia Antipolis, France

August 2004 to December 2004, Department of Electrical and Computer Engineering, University of Illinois at Urbana Champaign

**Teaching**

- Courses developed
  - Graphical Models and Inference - being developed during Spring 2016
  - Advanced topics in channel coding - a graduate course on the fundamentals of codes on graphs and iterative decoding (taught online through TTVN once)
  - Wireless Communications - upgraded the graduate level wireless communications course with emphasis on recent developments
- Courses taught - Channel coding for communications, Advanced Channel Coding, Information theory, Wireless communications, Digital Communications, Modulation Theory, Signals and Systems

**Main Research areas**

- Coding theory and information theory
- Applications to 5G wireless and Internet of Things
- Signal processing for big data
- Non-volatile memories and other emerging paradigms in data storage

**Student Supervision**

- Graduated Students - 15 Ph.D., 4 Ph.D. students were co-advised with other professors, 7 M.S students
- Current Students - 4 Ph.D.

**Editorial Positions**

- Associate Editor, Coding Techniques, IEEE Transactions on Information Theory, 2015-2018
- Area Editor, Coding Theory and Applications, IEEE Transactions on communications, 2007 - 2011
- Editor, IEEE Transactions on communications, 2005 - 2011
- Editor, IEEE Transactions on Wireless Communications, 2001 - 2005
- Associate editor for IEEE communications letters, 2000 - 2003

**Honors**

- Fellow of IEEE
- Outstanding Professor award, Department of Electrical & Computer Engg, Texas A&M University, Dec 2015
- Dean's excellence award, College of Engineering at Texas A&M University, May 2015
- Recipient of the Professional Progress in Engineering (Alumni award given to outstanding alumni under the age of 46) from Iowa State University, Ames, IA, April 2014
- Recipient of the Association of Former Students teaching award, Texas A&M University, 2012
- Recipient of the 2011-2012 William Keeler fellow award from the College of Engineering, Texas A&M University.
- Recipient of 2009 Halliburton fellowship from the College of Engineering, Texas A&M University.

- Recipient of the 2007 Best Paper Award from the IEEE Signal Processing for Data Storage society
- Outstanding Professor Award, Department of Electrical Engineering, Texas A&M University, 2002
- Faculty early CAREER development award, National Science Foundation, 2001
- Select Young Faculty Award within College of Engineering at Texas A&M University, 2001
- Nominated for Montague Teaching Excellence Award at Texas A&M University from Dept. of Electrical Engineering in Year 2001 (did not receive the award)
- Recipient of Harpole, Palmer and Litton Graduate Fellowship from the Dept. of Electrical Engineering, Iowa State University, Ames, IA
- Recipient of the Premium for Academic Excellence award from Graduate College, Iowa State University, Ames, IA
- Member of Phi Kappa Phi (1994-95)
- Graduated at the top of the class in bachelor's degree

**Externally  
Sponsored  
Research**

1. PI - K.R. Narayanan, Co-PIs - A. Karsilayan, E. Serpedin and J. Silva-Martinez, "EARS: Enhancing Radio-Frequency Spectrum Through Interference Resilient Cognitive Radio Systems: Design, Performance Analysis and Optimization", National Science Foundation, \$407,334, October 1, 2015 - September 31, 2018
2. PI - A. L. N. Reddy, Co-PI K.R. Narayanan and P. Gratz, "I/URC Phase II: Center on Intelligent Storage", National Science Foundation, \$300,000, September 15, 2014 - August 31, 2019
3. PI - M. Smotherman, Senior Personnel - K.R. Narayanan, "Networking Strategies used by Bats to Improve Social Sonar", National Science Foundation, \$660,000 (pro-rated amount \$30,433), August 15, 2014 to August 14, 2017
4. PI - B. Nazer, Co-PI - K. R. Narayanan (TEES PI) and B. Aazhang, "CIF: Medium: Collaborative Research: Interference-Aware Cooperation via Structured Codes", National Science Foundation, \$1,086,558, May 1, 2013 to April 1, 2017
5. PI - H. Pfister, Co-PI - K.R. Narayanan, "CIF:Small: Design and Analysis of Spatially-Coupled Coding Systems", National Science Foundation, \$494,231, August 1, 2013 to July 31, 2016
6. PI - J. Boutros, Co-PI - K.R. Narayanan and E. Viterbo, "Information Processing via Lattices in Digital Data Networks", Qatar National Research Foundation, \$1,045,374, October 15, 2012 - October 14, 2015
7. PI - A. Ruimi, Co-PI - K. R. Narayanan, M. Fratarcangeli, B. Nour, J. N. Reddy and A. R. Srinivasa, "Surgical Thread Simulation Using a Novel Information-Theory Approach", Qatar National Research Foundation, \$1,040,321, October 15, 2012 - October 14, 2015
8. PI - K.R. Narayanan, co-PI - H.D. Pfister, "Coding for Fiber-Optic Communication", Xilinx Inc, \$100,000, Sept 1, 2011 - May 31, 2013

9. PI - K.R. Rajagopal, Co-PI's - A.R. Srinivasa, J. Criscione and K.R. Narayanan, "CDI Type II/Collaborative Research: A New Approach to the Modeling of Clot Formation and Lysis in Arteries", National Science Foundation, \$875,165, (Pro-rated amount - \$214,114), Sept 1, 2010 - Aug 31, 2013
10. PI - J.F. Chamberland, Co-PI's - H.D. Pfister and K.R. Narayanan, "Fundamental Limits on Delay Constrained Communications", National Science Foundation, \$ 300,000, Sept 1, 2008 - Aug 31, 2011
11. PI - X. Zhang, Co-PIs - K.R. Narayanan (TEES PI), H. D. Pfister, "GOALI : Advanced Coding and Signal Processing for Magnetic Recording: From Theory to Implementation", National Science Foundation, \$175,000 (dollar amount awarded to Texas A&M University), May 1 2008 - April 31 2011
12. PI - M.S. Alouini, Co-PIs - K.R. Narayanan, H. D. Pfister, and K. Qaraqe, "Universal Signaling Schemes for Multimedia Transmission over Wireless Networks", Qatar National Research Foundation, \$575,100, January 2008-December 2010
13. PI - K.R. Narayanan, "Joint Source-Channel Coding for Wireless Networks", National Science Foundation, \$300,000, September 2007 - August 2010
14. PI - A. R. Srinivasa, Co-PIs - J. Froyd, A. Jayaraman, D. Maxwell, K.R. Narayanan, "CCLI Phase I: An Engineering Emphasis for Preparing Students for First-year Engineering Curricula", National Science Foundation, \$200,000, June 2009 - May 2011
15. PI - K.R. Narayanan, Co-PI H. D. Pfister, "Soft Decision Decoding of Reed Solomon Codes for High Density Recording Channels", International Storage Industry Consortium, \$32,000, January 2007 - December 2007
16. PI - K.R. Narayanan, "Understanding and Mitigating Error Floors in Iterative Decoding with Applications to Magnetic Recording", International Storage Industry Consortium, \$30,000, Jan 2006 - December 2006
17. PI - K.R. Narayanan, "Universal Source and Channel Codes for Non-Ergodic Channels", National Science Foundation, \$189,586, Sept. 2005 - Aug 2008
18. PI - K.R. Narayanan, "Low Complexity Iterative Decoding for Digital Magnetic Recording", Seagate Research Inc, Pittsburgh, \$75,106, 01/04-12/05.
19. PI - K.R. Narayanan, "Coding for Long Haul Optical Networks", Azea Networks, United Kingdom, \$45,000, Jan 01, 2004 to Dec 31, 2004
20. PI - K.R. Narayanan, CAREER award "Design and Analysis of Iteratively Decodable Codes for Wireless Communications and Digital Magnetic Recording", National Science Foundation, \$300,000, Feb 01, 2001 - Jan 31, 2007
21. PI - K. R. Narayanan, "Low Density Parity Check Codes and Turbo-Like Codes for Wireless Communications", National Science Foundation, \$179,925, 09/01/2000 - 08/31/2003
22. PI - K.R. Narayanan, "Advanced Coding Techniques and Low Complexity Decoding for Wireless Data Networks", Advanced Technology Program, Texas Higher Education coordinating Board, \$72,600, 01/01/00-12/31/01
23. PI - K. R. Narayanan, " Low Density Parity Check Codes for High Order Signal Constellations", Tadiran Microwave Networks, \$38,000, 06/01/00-12/01/01

24. PI - K.R. Narayanan, "Low Complexity Iterative Decoding for Digital Magnetic Recording", Seagate Research Inc, Pittsburgh, \$80,106, 01/02-12/03
25. PI - K. R. Narayanan, "Concatenated Coding and Iterative Decoding", Rockwell Collins University Grants Program, \$41,500, 09/01/00-05/01/02
26. PI - K.R. Narayanan, "Research in Turbo Coding", GS Labs, \$16,000
27. PI - K. R. Narayanan, "Low Density Parity Check Codes for Wireless Communications", Texas Instruments, \$20,000, 09/01/00-08/31/01
28. PIs - C.N.Georghiadis, S.Miller, K.Narayanan, & X. Wang, "Space-Time Processing", Motorola Inc, Austin, \$60,000, 01/01/2000-01/01/2001
29. PI - G. Choi, Co-PI - K.R. Narayanan, "ASIC Design and Implementation of Advanced Coding Techniques for Wireless Data Networks", Advanced Technology Program, Texas Higher Education coordinating Board, \$142,600, 01/01/02-12/31/03

**Internally  
Sponsored  
Research**

1. PI - C.N. Georghiadis, Co-PIs - S.Miller, K.Narayanan, E. Serpedin, X. Wang, Z. Xiong and B. Zoghi, "Advanced Algorithms for High Speed Wireless Networks", Telecommunications Infrastructure & Technology Fund, Texas A&M University, \$485,000, 08/01/01 - 07/31/04

**Book  
Chapters**

1. K.R. Narayanan, Turbo Equalization, in Wiley Encyclopedia on Telecommunications, Editor - J.G. Proakis, Wiley, 2003
2. J. Li, E. Kurtas and K.R. Narayanan, Turbo Product Codes for Partial Response Channels, in CRC Handbook, CRC Press

**Tutorials**

- Low Density Parity Check Codes, IEEE WCNC 2004
- Low Density Parity Check Codes, 2005, IEEE Web-based tutorial
- EXIT Charts: Properties, Extensions and Applications, Indian Institute of Science, 2005

**Patents**

1. K.R. Narayanan, J. Jiang and N. Nangare, "Iterative decoding of linear block codes by adapting the parity check matrix", U.S. Patent 7519898, April 2009
2. H.-J. Tarn, K.R. Narayanan, R.M. Rao, R. N. Mazareh, "Circuitry and method for forward error correction", U.S. Patent 9009577B1, April 2015

**Journal  
Publications  
Published  
and  
Submitted**

1. Y.Wang, K.R. Narayanan and Y.-C. Huang, "Concatenations of Polar Codes with Outer BCH and Convolutional Codes", *accepted to the IEEE Journal of Selected Areas in Communication*, 2015
2. Y.-C. Huang, K.R. Narayanan and T. Liu, "Coding for Parallel Gaussian Bidirectional Channels: A Deterministic Approach", *to appear in the IEEE Transactions on Information Theory*
3. E. Tunali, Y.-C. Huang, J. Boutros and K.R. Narayanan, "Lattices over Eisenstein Integers for Compute-and-Forward", *to appear in the IEEE Transactions on Information Theory*, Oct. 2015

4. P.-C. Wang, Y.-C. Huang, and K.R. Narayanan, "Asynchronous Physical Layer Network Coding", *IEEE Journal of Selected Areas in Communication*, pp. 309-322, Vol. 33, No. 2, 2015
5. B. Hern and K.R. Narayanan, "Multilevel Coding Schemes for Compute-and-Forward with Flexible Decoding", *IEEE Transactions on Information Theory*, pp. 7613-7631, Vol. 59, No. 11, Nov. 2013
6. Y.-C. Huang, N. E. Tunali and K.R. Narayanan, "A Compute-and-Forward Scheme for Gaussian Bi-Directional Relaying with Inter-Symbol Interference", *IEEE Transactions on Communications*, pp. 1011-1019, Vol. 61, No. 3, March 2013
7. A. Khisti, B. Hern and K.R. Narayanan, "On Modulo-Sum Computation over an Erasure Multiple Access Channel", *IEEE Transactions on Information Theory*, pp. 4129-4138, Vol. 59, No. 7, Aug. 2013
8. A. Yedla, H.D. Pfister and K.R. Narayanan, "Code Design for the Noisy Slepian-Wolf Problem", *IEEE Transactions on Communications*, pp. 2535-2545, Vol. 61, No. 6, June 2013
9. Y.C. Huang and K.R. Narayanan, "Joint Source-Channel Coding with Correlated Interference", *IEEE Transactions on Communications*, pp. 1315-1327, Vol. 60, No. 5, May 2012
10. K.R. Narayanan and A. R. Srinivasa, "A Shannon Entropy based Non Equilibrium 'Entropic' Temperature of a General Distribution", in *Physical Review Letters E*, pp. 031151-1-031151-11, Vol. 85, No. 3, March 2012
11. S. Doraiswamy, K.R. Narayanan and A. R. Srinivasa, "Finding Minimum Energy Configurations for Constrained Beam Buckling Problems using the Viterbi Algorithm", *International Journal of Solids and Structures*, pp. 289-297, Vol. 49, No. 2, Jan. 2012
12. P.S. Nguyen, H. Pfister and K.R. Narayanan, "A Rate Distortion Perspective on Multiple Error and Erasure Decoding of Reed Solomon Codes", *IEEE Transactions on Information Theory*, pp. 668-691, Vol. 57, No. 2, February 2011
13. M. P. Wilson, K.R. Narayanan, H. Pfister and A. Sprintson, "Joint Physical Layer and Network Coding for Bi-Directional Relaying", *IEEE Transactions on Information Theory*, pp. 5641-5654, Vol. 56, No. 11, November 2010
14. M. P. Wilson, K.R. Narayanan and G. Caire, "Joint Source-Channel Coding with Side Information using Hybrid Digital Analog Codes", *IEEE Transactions on Information Theory*, pp. 4922-4940, Vol. 56, No. 10, October 2010
15. K. Bhattad and K.R. Narayanan, "A Note on the Rate of Decay of Mean-Square Error with SNR for the AWGN Channel", *IEEE Transactions on Information Theory*, pp. 332-335, Vol. 56, No. 1, January 2010
16. K.R. Narayanan and A. R. Srinivasa, "Using discrete optimization algorithms to find minimum energy configurations of slender cantilever beams with non-convex energy functions", *Mechanics Research Letters*, Pages 811-817, September 2009
17. J. Jiang and K.R. Narayanan, "Bit Level Algebraic Soft Decision Decoding", *IEEE Transactions on Information Theory*, 2008, Vol. 54, No. 9, pp. 3907-3928, September 2008

18. K. Bhattad, K.R. Narayanan, and G. Caire, "On the Distortion Exponent of Some Layered Transmission Schemes", *IEEE Transaction on Information Theory*, Vol. 54, No. 7, pp. 2943-2958, July 2008
19. G. Caire and K.R. Narayanan, "On the Distortion SNR Exponent of Hybrid Digital Analog Space Time Coding", *IEEE Transactions on Information Theory*, pp. 2867-2877, Vol. 53, No. 8, August 2007
20. K. Bhattad and K.R. Narayanan, "An MSE Based Transfer Chart for Analyzing Iterative Decoding Schemes", *IEEE Transaction on Information Theory*, pp. 22-38, Vol. 53, No. 1, Jan 2007
21. B. J. Peiris, K. R. Narayanan and S. L. Miller, "A Reduced Complexity Spectral Domain Approach to Design Spreading Sequences for DS-CDMA Systems in Frequency Selective Fading Channels," *IEEE Transactions on Wireless Communications*, pp. 2386-2395, Vol. 5, No. 9, Sept 2006
22. J. Jiang and K. R. Narayanan, "Iterative Soft Input Soft Output Decoding of Reed-Solomon Codes", *IEEE Transactions on Information Theory*, pp. 3746-3756, Vol. 52, No.8, August 2006
23. C. F. Lan, Z. Xiong and K.R. Narayanan, "Source-optimized Irregular Repeat Accumulate Codes with Inherent Unequal Error Protection Capabilities and Their Application to Scalable Image Transmission", *IEEE Tran. Image Processing*, pp. 1740-1750, Vol. 52, No. 7, July 2006
24. K. Bhattad and K. R. Narayanan, "A Decision Feedback Based Scheme for Slepian-Wolf Coding of Sources With Hidden Markov Correlation," *IEEE Communications Letters*, pp. 378-380, Vol. 10, No. 5, May 2006
25. N. Nangare, X. Yang, E. Kurtas and K.R. Narayanan, "Performance of BCJR-DFE Detectors Over Recording Channels Using Pattern-Dependent Noise Prediction", *IEEE Tran. Magnetics*, pp. 2971-2973, vol. 41, no. 10, Oct. 2005
26. N.D. Doan, and K.R. Narayanan, "Design of Good Low Rate Codes for ISI Channels Based on Spectral Shaping", *IEEE Tran. Wireless Comm*, vol. 4, no. 5, pp. 2309-2317, Sept. 2005
27. H. Sankar and K.R. Narayanan, "Design of irregular repeat accumulate codes for OFDM systems with partial channel state information", *IEEE Tran. Wireless Communications*, pp. 2491-2497, Vol. 5, No. 4, Sept. 2005
28. X. Wang, G. Yue and K.R. Narayanan, "Optimization of LDPC Coded Turbo CDMA Systems", *IEEE Transactions on Signal Processing*, Vol. 53, No. 4, pp. 1500-1510, April 2005
29. K.R. Narayanan, X. Wang and G. Yue, "Estimating the PDF at the Output of the SIC/MMSE Equalizer and Its Applications in Designing LDPC Codes" *IEEE Tran. Wireless Communications*, Vol. 4, No. 1, pp. 278-287, Jan. 2005
30. J. Li, K.R. Narayanan and C.N. Georghiadis, "An Efficient Algorithm to Compute the Euclidean Distance Spectrum of a General Intersymbol Interference Channel and Its Applications", *IEEE Tran. Communications*, Vol. 52, No. 12, pp. 2041-2046, Dec. 2004
31. H. Sankar and K.R. Narayanan, "Memory Efficient Implementation of Low Density Parity Check Codes", in *IEEE Tran. Communications*, vol. , no. , pp. 1225-1230, Aug. 2004

32. C. F. Lan, T. Chu, K.R. Narayanan and Z. Xiong, "Scalable Image Transmission Using Rate Compatible Irregular Repeat Accumulate Codes", *IEEE Tran. Communications*, vol. 52, no. 7, pp. 1092-1101, July 2004
33. J. Jiang and K.R. Narayanan, "Iterative Soft Decision Decoding of RS Codes", *IEEE Communications Letters*, Vol. 8, No. 4, pp. 244-246, April 2004
34. J. Li, Y. Cai, K.R. Narayanan, G. Lenner, A. Lucero, A. Pilipetskii and C. N. Georghiades, "On the Bit Error Rate of Product Accumulate Codes in Optical Fiber Communications", *IEEE Journal. of Lightwave Technology*, Vol. 22, No.2, pp. 640-646, Feb. 2004
35. J. Li, K.R. Narayanan, C.N.Georghiades, "Product Accumulate Codes: A Class of Low Complexity Capacity Approaching Codes", *IEEE Tran. Info Theory*, Vol. 50, No. 1, pp. 31-46, Jan 2004
36. K.R. Narayanan, I. Altunbas and R. Narayanaswami, "Design of Codes for Minimum Shift Keying based on Density Evolution", *IEEE Tran. Communications*, pp. 1283-1295, Vol. 51, No. 8, Aug. 2003.
37. A. Mondragon, K.R. Narayanan, and E. Sanchez-Sinencio, "Floating Gate Analog Implementation of the Additive Soft-Input Soft-Output Decoding Algorithm", *IEEE Tran. Circuits and Systems*, pp. 1256-1269, Vol. 50, No. 10, Oct. 2003.
38. V. Gulati and K.R. Narayanan, "Concatenated Codes for Fading Channels Based on Recursive Space-Time Codes", in *IEEE Tran. Wireless Communications*, pp. 118-128, Vol. 2, Jan 2003.
39. S. Choe, C.N. Georghiades and K.R. Narayanan, "Improved Upper Bounds on Error Probability for Biorthogonal Trellis-Coded CDMA Systems", *IEEE Commun. Letters*, pp. 361-363, Vol.6, No.9, Sept. 2002.
40. A. Prabhakar and K.R. Narayanan, "Pseudo-Random Construction of Low Density Parity Check Codes using Linear Congruential Sequences", *IEEE Tran. Communications*, pp. 1389-1396, Vol. 50, No.9, September 2002 .
41. J. Li and K.R. Narayanan, "On the Performance of Single Parity Check based Turbo Product Codes and LDPC codes on Partial Response Channels", *IEEE Tran. Communications*, pp.723-734, vol. 50, no. 5, May, 2002.
42. N. D. Doan and K.R. Narayanan, "Iterative Retransmission Schemes for Inter Symbol Interference Channels", *IEEE Tran. Commun*, pp. 560-571, vol. 50, no. 4, April 2002.
43. B.Lu, X. Wang and K.R. Narayanan, "LDPC based Space-time Coded OFDM Systems over Correlated Fading Channels: Analysis and Receiver Design", *IEEE Tran. Communications*, pp. 74-88, vol. 50, no. 1, Jan. 2002.
44. I. Altunbas and K.R. Narayanan, "A Novel Serial Concatenated Coding Scheme with Minimum Shift Keying", in *IEE Electronics Letters*, pp.1393-1395, vol. 37, no. 23, Nov. 2001
45. U. Dasgupta and K. R. Narayanan, "Parallel Decoding of Turbo Codes using Soft Output  $M$  algorithms", *IEEE Comm. Letters*, pp.352-354, vol. 5, no. 8, August 2001.
46. J. Li, E. Kurtas, K.R. Narayanan, and C.N. Georghiades, "On the Performance of Turbo Product Codes for Partial Response Channels", *IEEE Tran. Magnetics*, pp. 1932-1934, vol. 37, no. 4, July 2001



47. K. R. Narayanan, "Effect of Precoding on the Convergence of Turbo Equalization for Partial Response Channels", *IEEE Journal Sel. Areas in Communications*, pp.686-698, vol. 19, no. 4, April 2001.
48. K. R. Narayanan & G. L. Stüber, "Performance of Trellis Coded CPM with Iterative Demodulation and Decoding", *IEEE Trans. Communications*, pp. 676-687, vol. 49, no. 4, April 2001.
49. L. McPheters, S. W. McLaughlin, & K.R. Narayanan, "Precoded PRML, Serial Concatenation, and iterative (turbo) decoding for digital magnetic recording", *IEEE Trans. Magnetics*, pp. 2325–2327, vol. 35, no. 5, Part:I, Sept. 1999
50. K.R.Narayanan & G.L.Stüber, "A Serial Concatenation Approach to Iterative Demodulation and Decoding," *IEEE Trans. Comm.*, pp. 956-961, vol. 47, no. 7, July 1999
51. K.R.Narayanan & G.L.Stüber, "List Decoding of Turbo Codes," *IEEE Trans. Comm.*, pp. 754-762, vol. 46, no. 6, June 1998
52. K.R.Narayanan & G.L.Stüber, "Selective Serial Concatenation of Turbo Codes," *IEEE Comm. Letters*, pp. 136-139, vol. 1, no. 5, Sep. 1997
53. K.R. Narayanan & J.F.Doherty, "A Convex Projections Method for Improved Narrowband Interference Rejection in Direct Sequence Spread Spectrum Systems", *IEEE Trans. Comm*, pp. 772-774, vol. 45, no. 7, July 1997
54. K.R.Narayanan & G.L.Stüber, "A Novel ARQ Technique Based on the Turbo Coding Principle," *IEEE Comm. Letters*, pp. 49-51, vol. 1, no. 2, Feb. 1997

**Invited  
Talks**

1. (upcoming) Key note talk at the 2016 ICC workshop on Massive Uncoordinated Multiple Access
2. "Coding-Inspired Massive Multiple Access for Next-Generation Wireless Networks", Samsung Research, Dallas, December 2015
3. "Uncoordinated Massive Multiple Access", Chinacom, August 2015
4. "Half Product Codes for Flash Memory", Intel, April 2015
5. "Half Product Codes for Flash Memory", Hitachi, San Jose, March 2015
6. "Uncoordinated Massive Multiple Access", Information Theory at Its Applications Workshop, San Diego, Feb 2015
7. "Lattices from Codes for Harnessing Interference : An Overview and Some Generalizations", IEEE Information Theory Workshop, Tasmania, Australia, Nov. 2014
8. "Coding and Modulation for Physical Layer Network Coding", Signal Processing and Communications Conference, Indian Institute of Science, July 2014
9. "Multilevel Lattice Codes for the Interference Channel", IEEE Communication Theory Workshop, May 2014
10. "Physical Layer Network Coding", ECE Seminar, Iowa State University, April 2014
11. "Lattice Codes based on Product Constructions", Information Theory and its Applications Workshop, Feb 2014

12. "Lattices based on Eisenstein Integers for Compute and Forward", *50th Allerton Conference on Communications, Control and Computing*, 2012
13. "Codes based on Eisenstein Integers for Compute and Forward", Information Theory and Applications Workshop, San Diego, 2012
14. "Coding for Compute and Forward", Banff International Research Station (BIRS) workshop on Algebraic Structure in Network Information Theory, August 2011
15. "Coding for Compute and Forward", Signal Processing and Communications Conference SPCOM 2010, University of Hawaii, November 2010
16. "Coding for Compute and Forward", Signal Processing and Communications Conference SPCOM 2010, Indian Institute of Science, Bangalore, July 2010
17. "Coding for Compute and Forward", Indian Institute of Technology, Madras, July 2010
18. "Joint Physical Layer and Network Layer Coding for Bi-Directional Relaying", Department of Electrical and Computer Engineering, Texas A&M University, Doha, Qatar, May 2010
19. "Joint Physical Layer and Network Layer Coding for Bi-Directional Relaying with Asymmetric Links", Information Theory and its Applications workshop, University of California at San Diego, La Jolla, CA, January 2009
20. "Joint Physical Layer and Network Layer Coding for Bi-Directional Relaying", Department of Electrical and Computer Engineering, University of Massachusetts, Amherst, November 2008
21. "Joint Physical Layer and Network Layer Coding for Bi-Directional Relaying", Department of Electrical and Computer Engineering, University of Arizona, Tucson AZ, February 2008
22. "On the Thermodynamic Temperature of a General Distribution", Fisher Information Workshop, Department of Optics, University of Arizona, Tucson, AZ, February 2008
23. "Joint Source Channel Coding for Non-Ergodic MIMO Channels", WINLAB Seminar, Rutgers University, April 24, 2007
24. "Joint Source Channel Coding for Non-Ergodic MIMO Channels", Columbia University, April 23, 2007
25. "Joint Source Channel Coding for Non-Ergodic MIMO Channels", MIT LIDS Colloquium, April 3, 2007
26. "Hybrid Digital Analog Coding with Side Information", UCSD Workshop on Information Theory and its Applications, February 2007
27. "Bit Level Soft Decision Decoding of Reed Solomon Codes", American Mathematical Society meeting, New Orleans, LA, January 2007
28. "Hybrid Digital Analog Strategies for Broadcasting", Rice University, TX, October 2006
29. "Iterative Signal Processing versus Decision Feedback Signal Processing", ICCSS, March 2006
30. "Hybrid Digital and Analog Space-Time Coding", The University of Texas at Austin, Feb 2006

31. "Further Results on the SNR Exponent of Hybrid Digital Analog Space-Time Codes", UCSD Workshop on Information theory, Feb 2006
32. "On the SNR Exponent of Hybrid Digital Analog Space-Time Codes", Indian Institute of Science, Bangalore, India, December 2005
33. "Soft Decision Decoding of RS Codes", Indian Institute of Science, Bangalore, India, June 2005 and again in December 2005 with updated results.
34. "To Iterate or Feedback? How to Achieve Universally Good Performance on Channels with Memory", Indian Insitute of Science, Bangalore, India, June 2005.
35. "To Iterate or Feedback? How to Achieve Universally Good Performance on Channels with Memory", Indian Institute of Technology, Madras, India, June 2005.
36. "To Iterate or Feedback? How to Achieve Universally Good Performance on Channels with Memory", Georgia Insitute of Technology, Lorraine, Metz, France, April 2005
37. "Soft Decision Decoding of RS Codes", Ecole Nationale Superieur des Telecommunications (ENST) - Paris, France, April 2005
38. "To Iterate or Feedback? How to Achieve Universally Good Performance on Channels with Memory", Ecole Nationale Superieur des Telecommunications (ENST - Paris), Paris, France, April 2005.
39. "Soft Decision Decoding of RS Codes", Ecole Nationale Superieur des Telecommunications (ENST) - Bretagne, France, March 2005
40. "To Iterate or Feedback? How to Achieve Universally Good Performance on Channels with Memory", Institut Eurecom, Sophia Antipolis, France, Jan 2005.
41. "Soft Decision Decoding of RS Codes", University of Notre Dame, IN, November 2005
42. "To Iterate or Feedback? How to Achieve Universally Good Performance on Channels with Memory", The University of Texas at Austin, October 2004
43. "Decision Feedback Signal Processing for Achieving Near Capacity Performance on Channels with Memory", The University of Illinois at Urbana-Champaign, October 2004
44. "Iterative Decoding of RS Codes Based on Adaptive Parity Check Matrices", March 2004, Worskhop on Theoretical Advances in Information Recording, DI-MACS, Rutgers University
45. "Simplified Design and Analysis of LDPC Codes for ISI Channels with Turbo Equalization", April 2003, Harvard University.
46. "Design of LDPC codes with MMSE Turbo Equalization", Information Theory Workshop, October 2002
47. "Product Accumulate Codes on Fading Channels", Asilomar Conference on Signals and Systems, November 2002
48. "Design and Analysis of Codes for Turbo Equalization with Optimal and Sub-optimal Soft Output Decoding", Allerton conference on communications and control, October 2002 (upcoming)

49. "Product Accumulate Codes: Construction and Properties", Information Theory Workshop, September 2001
50. "Effect of Precoding on the Convergence of Turbo Equalization for Partial Response Channels", The Magnetic Recording Conference, Santa Clara, August 2000
51. "Design considerations for iterative equalization and decoding", Conference on Information Science and Systems (CISS), Princeton, Mar 2000
52. "Iterative demodulation, equalization and decoding", IEEE signal processing society, Dallas TX, January 20, 2000
53. "Performance of Turbo Coded CPM with Iterative Demodulation", IEEE Military Commun. Conf, 1999
54. "Turbo coding and Low density parity check codes for microwave communications", Tadiran Microwave Networks, Houston, TX, December 2, 1999

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- GLS Labs, Atlanta, GA
- Microwave Networks, Stafford, TX, 2004
- Wilmer Hale Associates LLP, Boston, MA
- SiRF Technologies, San Jose, CA
- sw4a Inc
- Xilinx Inc

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- Board of Governors, IEEE Information Theory Society, 2016-2017
- Technical Program Co-chair, 2010 IEEE International Symposium on Information Theory.
- Lead guest editor, IEEE Journal of Selected Areas on Communications special issue on "Equalization with Applications to Wireless Communications", February 2008
- General Co-Chair, IEEE Information Theory Workshop 2007 in Lake Tahoe, CA, September 2007
- Ph.D. thesis reviewer for the Indian Institute of Science, Australia National University, Tel Aviv University, University of Toronto, University of Oulu, Norwegian University of Science and Technology
- Proposal reviewer and/or panelist for the National Science Foundation, Israel Ministry of Science, MICRO proposals for state of California, State of S. Carolina, Hongkong ministry of science, SABIC proposals for King Fahd University of Petroleum and Mines, Saudi Arabia, NSERC in Canada
- Member of best student paper award committee, IEEE signal processing society, data storage sub committee, Member of best paper award committee IEEE Comsoc.

**Conference  
Publications  
(Partial  
List)**

1. P.-C. Wang, Y.-C. Huang, K.R. Narayanan and J.J. Boutros, "Physical-Layer Network Coding over Block Fading Channels with Root-LDA Lattice Codes", *submitted to IEEE ICC 2016*.

2. S. Kumar, A. Vem, K.R. Narayanan and H.D. Pfister, "Spatially-Coupled Codes for Write-Once Memories", *in proceedings of the Allerton Conference on Communications Control and Computing*, 2015
3. N. T. Janakiraman, S. Emmadi, K. R. Narayanan and K. Ramchandran, "Exploring Connections between Sparse Fourier Transform Computation and Product Codes", *in proceedings of the Allerton Conference on Communications Control and Computing*, 2015
4. Y.-C. Huang, K.R. Narayanan, P.-C. Wang "Adaptive Compute-and-Forward with Lattice Codes over Algebraic Integers", *in proceedings of the Intl. Symp. Info. Theory*, 2015
5. S. Madala and K.R. Narayanan, "Uncoordinated Rate Selection: Approaching the Capacity of Gaussian MAC without Coordination", *in proceedings of IEEE ICC, Workshop on Massive Multiple Access*, pp. 2057-2062, June 2015
6. S. Emmadi, K. R. Narayanan and H.D. Pfister, "Half Product Codes for Flash Memory", *in Proceedings of the Non-Volatile Memories Workshop*, Feb 2015
7. Y. Wang and K.R. Narayanan, "Concatenations of Polar Codes with Outer BCH Codes and Convolutional Codes", *in proceedings of the Allerton Conference on Communications Control and Computing*, 2014
8. A. Vem, Y.-C. Huang, K.R. Narayanan and H.D. Pfister, "Multilevel Lattices based on Spatially-Coupled LDPC Codes with Applications", *in proceedings of the IEEE International Symposium on Information Theory*, pp. 2336-2340, July 2014
9. S. Kumar, A. Vem, K.R. Narayanan and H.D. Pfister, "Spatially-Coupled Codes for Side-Information Problems", *in proceedings of the IEEE International Symposium on Information Theory*, pp. 516-520, July 2014
10. Y. Jian, H.D. Pfister, K.R. Narayanan, R. Rao and R. Mazareh, "Iterative Hard Decision of Braided BCH Codes for High Speed Optical Communication", *in proceedings of IEEE Globecom*, 2013
11. B. Hern and K.R. Narayanan, "An Analysis of Joint Compute-and-Forward Decoder for the Binary Input Two-Way Relay Channel", *in proceedings of the Allerton Conference on Communications Control and Computing*, October 2013
12. Y.-C. Huang and K.R. Narayanan, "Lattice Codes Based on Product Constructions over  $F_q^2$  with Applications to Compute-and-Forward", *in proceedings of the IEEE Information Theory Workshop*, Sept. 2013
13. E. Tunali, K.R. Narayanan and H.D. Pfister, "Spatially-Coupled Low Density Lattices Based on Construction A with Applications to Compute-and-Forward", *in proceedings of the IEEE Information Theory Workshop*, Sept. 2013
14. B. Hern and K. R. Narayanan, "Joint Compute and Forward for the Two way Relay Channel with Spatially Coupled LDPC codes", *in Proc. of IEEE Globecom*, Nov. 2012, available for download from <http://arxiv.org/abs/1205.5904>
15. P. S. Nguyen, A. Yedla, H. D. Pfister, and K. R. Narayanan, "On the Maximum *a posteriori* Decoding Thresholds of Multiuser Systems with Erasures", *in Proc. IEEE International Symposium on Information Theory*, pp. 2701-2705, July 2012

16. K. R. Narayanan and H. D. Pfister, "Iterative Collision Resolution for Slotted ALOHA: An Optimal Uncoordinated Transmission Policy", in *Proc. International Symposium on Turbo Codes*, August 2012.
17. P. S. Nguyen, A. Yedla, H. D. Pfister, and K. R. Narayanan, "Threshold Saturation of Spatially-Coupled Codes on Intersymbol-Interference Channels", in *Proc. of IEEE ICC 2012*
18. N. E. Tunali and K. Narayanan, "Concatenated Signal Codes with Applications to Compute and Forward", in *Proc. IEEE Globecom Houston, TX, Dec. 2011*
19. Y.-C. Huang, N. E. Tunali, and K. Narayanan, "On the exchange rate for bi-directional relaying over inter-symbol interference channels", in *Proc. IEEE Globecom, Houston, TX, Dec. 2011*
20. Y.-C. Huang, K. Narayanan, and T. Liu, "Coding for parallel Gaussian bi-directional relay channels: A deterministic approach", in *Proc. Allerton Conference on Communications, Control and Computing, Sept. 2011*
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27. P. Parag, J.-F. Chamberland, H. Pfister, and K.R. Narayanan, "Code Rate, Queueing Behavior and the Correlated Erasure Channel", in *the proceedings of the Information Theory Workshop*, 2010
28. M. P. Wilson and K.R. Narayanan, "Power Allocation Strategies and Lattice Coding Based Coding Schemes for Bi-Directional Relaying", *Proceedings of IEEE International Symposium on Information Theory*, Seoul, 2009
29. P.S. Nguyen, H.D. Pfister and K.R. Narayanan, "A Rate-Distortion Perspective on Multiple Decoding Attempts for Reed Solomon Codes", *Proceedings of the Allerton Conference on Communications, Control and Computing*, 2009
30. A. Yedla, H.D. Pfister and K.R. Narayanan, "Can Iterative Decoding of Erasure Correlated Source be Universal?" *Proceedings of the Allerton Conference on Communications, Control and Computing*, 2009

31. K.R. Narayanan, M.P. Wilson and A. Sprintson, "Joint Physical Layer and Network Coding for Bi-Directional Relaying", *Proc. Allerton Conference on Communications Control and Computing*, October 2007
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33. M. Wilson, K.R. Narayanan and G. Caire, "Joint Source Channel Coding with Side Information Using Hybrid Digital Analog Codes", *Proc. 2nd ITA Workshop*, Jan 2007
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68. K.R. Narayanan and J. Li, "Bandwidth Efficient Low Density Parity Check Codes Using Multilevel Coding and Iterative Multi-stage Decoding", in *Proc. Intl. Symposium on Turbo Codes and Applications*, Brest, France, September 2000.
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## References

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